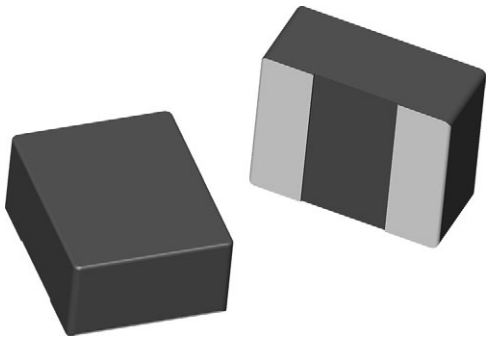
Commercial Power Inductor, Low DCR



FEATURES

- 2.5 mm x 2.0 mm x 1.2 mm SMD package
- Handles high transient current spikes without saturation
- Magnetically shielded composite construction
- Bottom plated terminals allow for a smaller pad layout for compact board spacing
- Packaging information: SMD packaging
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

RoHS COMPLIANT

HALOGEN FREE GREEN (5-2008)

LINKS TO ADDITIONAL RESOURCES

Product Page

APPLICATIONS

- SSD modules
- DC/DC converter for CPU
- Noise suppression and filtering
- Data networking and storage systems

STANDARD ELECTRICAL SPECIFICATIONS

PART NUMBER	L_0 INDUCTANCE ± 20 % AT 0 A (µH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) [1]	SATURATION CURRENT DC TYP. (A)	
					20 % DROP [2]	30 % DROP [3]
IHLL1008ABEZR33M1Z	0.33	14.0	19.0	6.0	7.3	8.5
IHLL1008ABEZR47M1Z	0.47	17.0	21.0	6.1	6.0	7.3
IHLL1008ABEZR68M1Z	0.68	25.0	30.0	5.5	5.6	6.3
IHLL1008ABEZ1R0M1Z	1.0	35.0	42.0	4.2	3.7	5.0
IHLL1008ABEZ1R5M1Z	1.5	53.0	61.0	3.6	3.0	3.6
IHLL1008ABEZ2R2M1Z	2.2	68.0	82.0	3.0	3.0	3.3
IHLL1008ABEZ3R3M1Z	3.3	110.0	135.0	2.1	2.0	2.8
IHLL1008ABEZ4R7M1Z	4.7	160.0	190.0	1.8	1.8	2.4

Notes

- All test data is referenced to 25 °C ambient
- Test condition: 1 MHz, 1 V
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

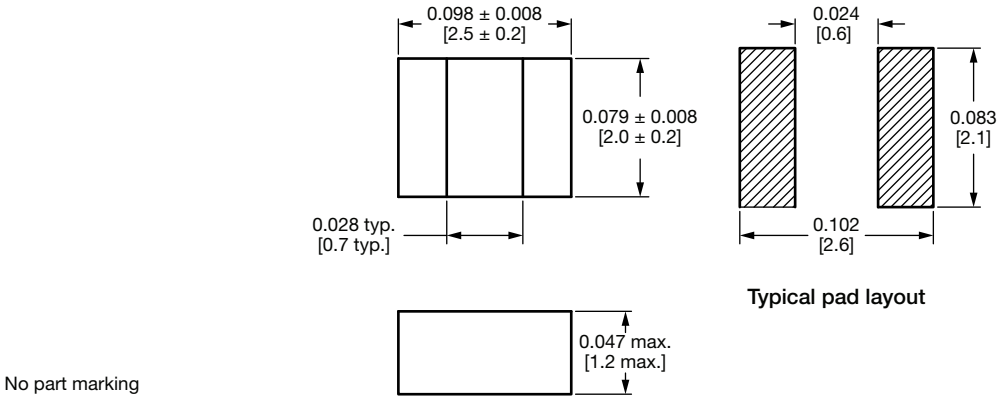
(1) DC current (A) that will cause an approximate ΔT of 40 °C
(2) DC current (A) that will cause L_0 to drop approximately 20 %
(3) DC current (A) that will cause L_0 to drop approximately 30 %

PATENT(S): www.vishay.com/patents
This Vishay product is protected by one or more United States and international patents.

For technical questions, contact: magnetics@vishay.com

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DIMENSIONS in inches [millimeters]



Typical pad layout

No part marking

DESCRIPTION

IHLL-1008AB-1Z	2.2 µH	± 20 %	EZ	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

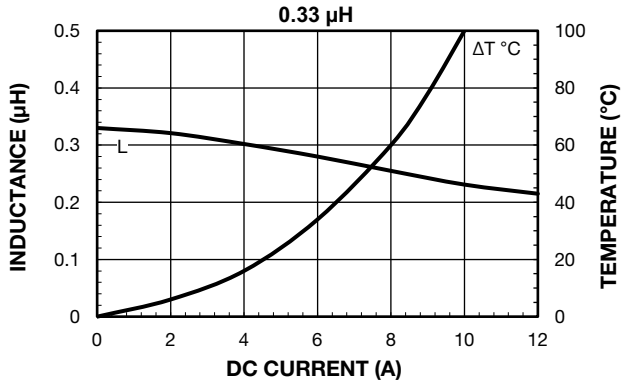
GLOBAL PART NUMBER

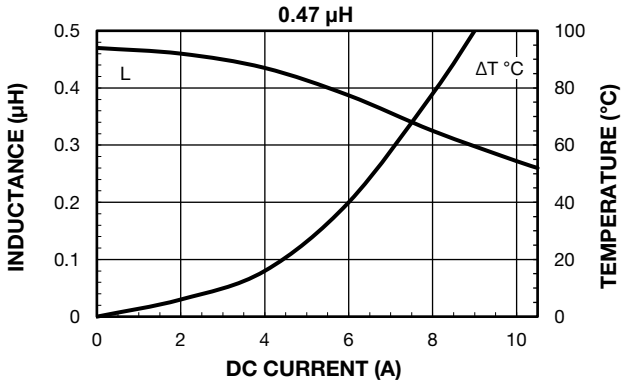
I H L L	1 0 0 8 A B	E Z	2 R 2	M	1 Z
PRODUCT FAMILY	SIZE	PACKAGE CODE	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	SERIES
		EZ = tape and reel	**2R2** = 2.2 µH	**M** = ± 20 %	

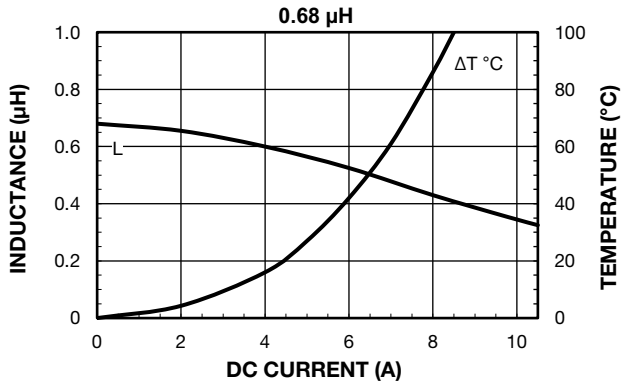
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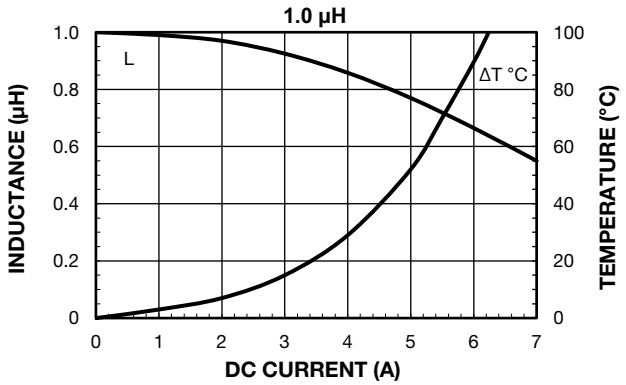
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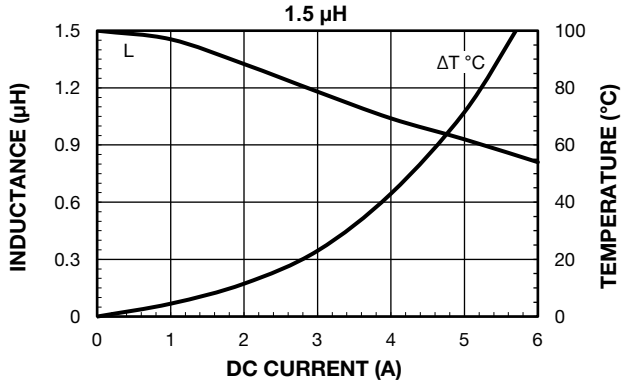
PERFORMANCE GRAPHS

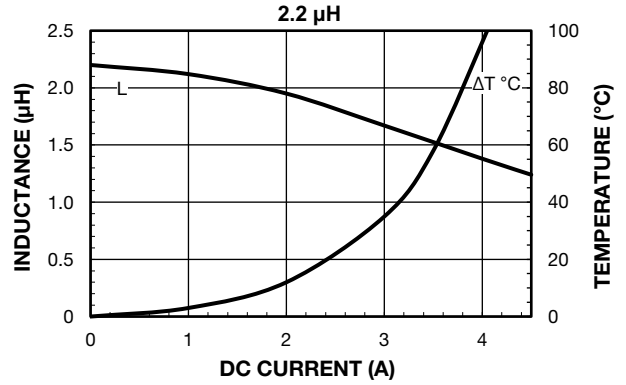


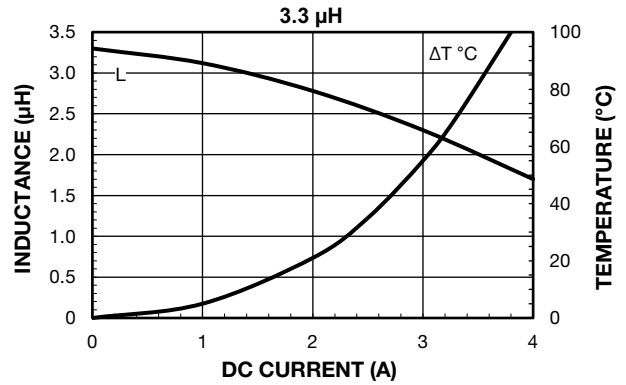


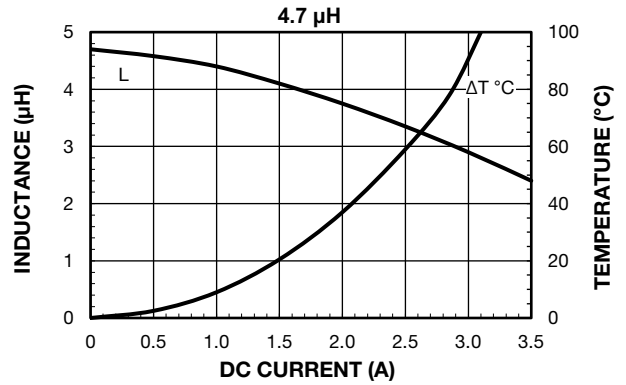












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